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FIG. 1

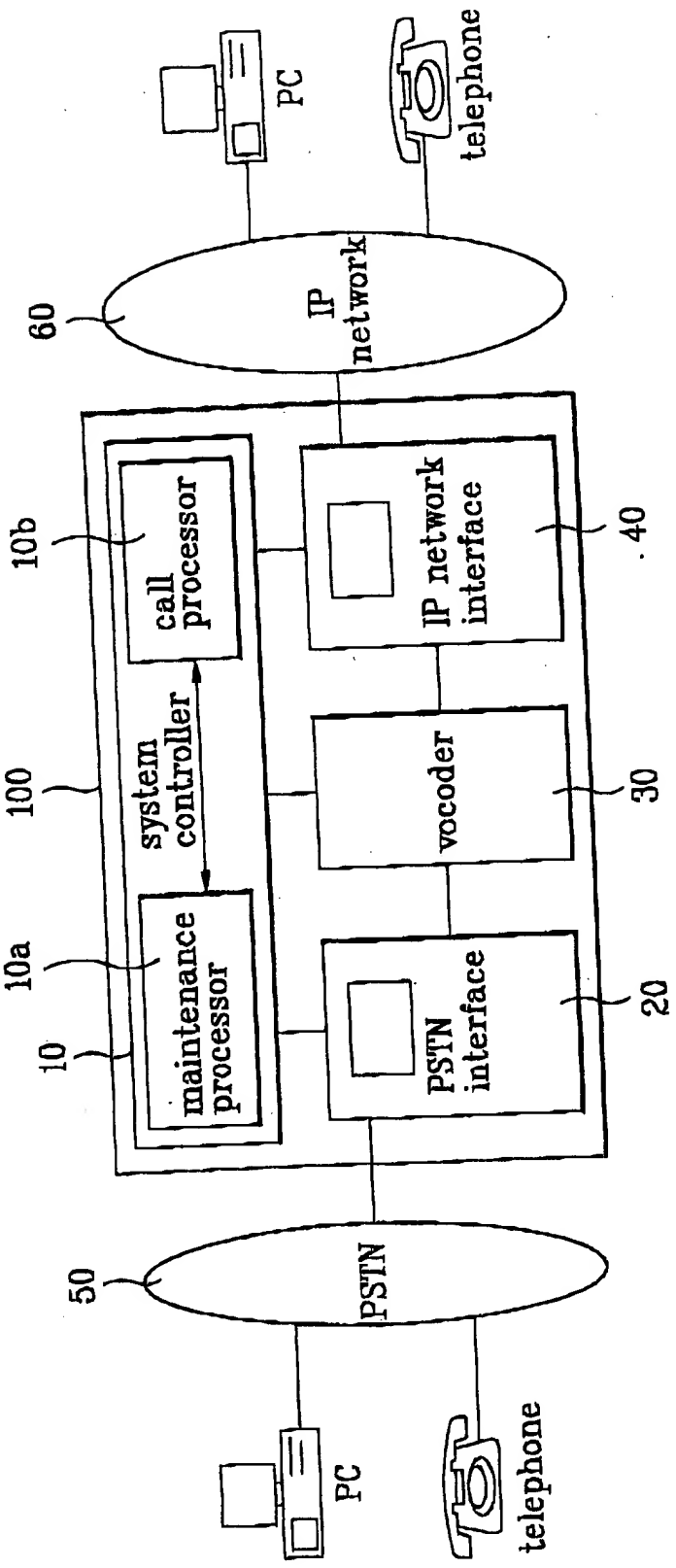

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FIG. 2

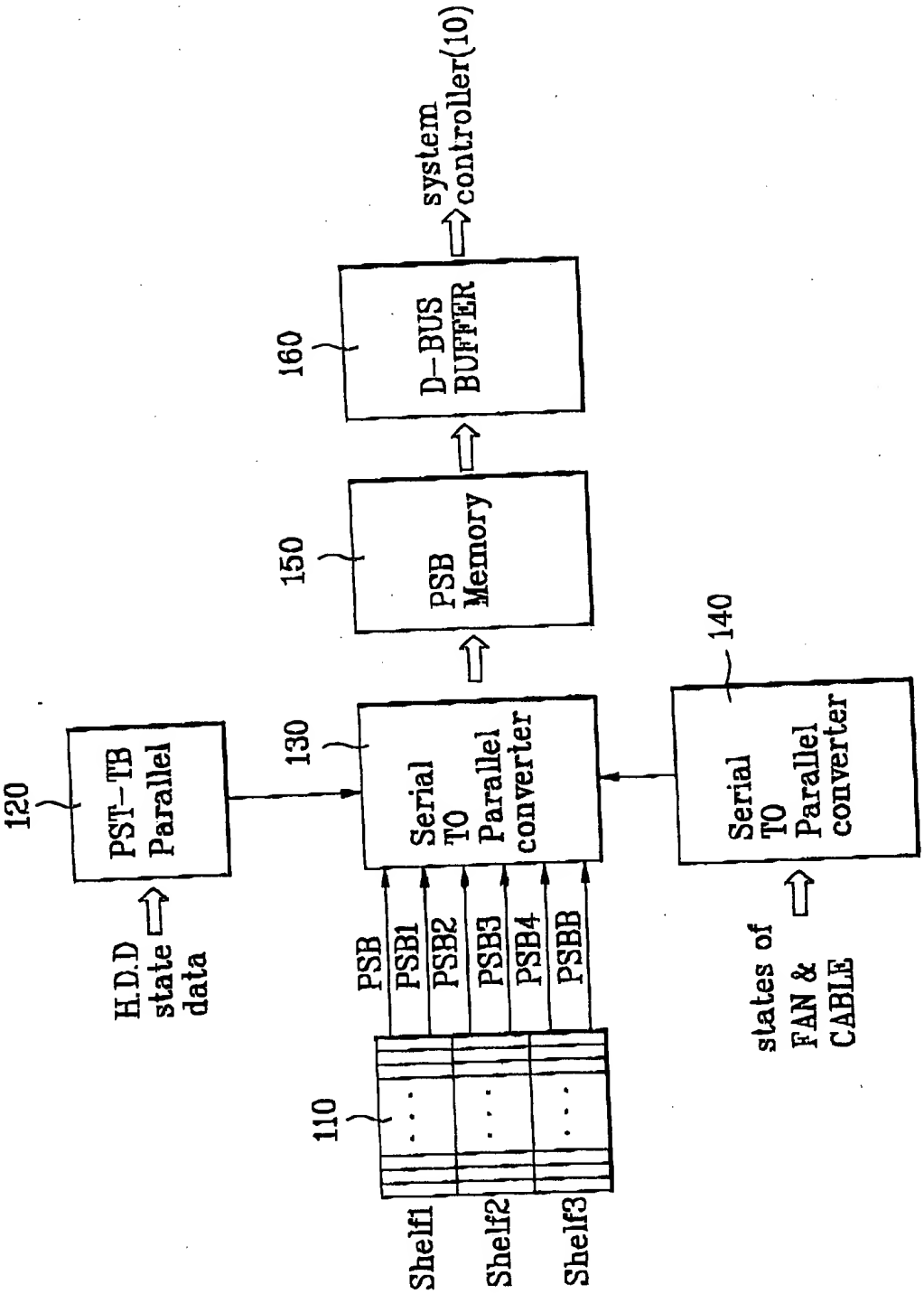

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FIG. 3A

	Add	b7~b4	b3~b0	Remarks
shelf	0000 0001 0002 0003 0004 0005 0006 0007	DPM1 LC2 LC4 LC6 LC8 RDCC1 DKU3,4 PSU0,1	DPM1 LC1 LC3 LC5 LC7 RDCC0 (HUBC) DKU1,2 DKU5,6	DKU,PSU : PSB_TB conversion from PSB-TB to PSB
shelf	0008 0009 000a 000b 000c 000d 000e 000f	DPM2 LC2 LC4 LC6 LC8 RDCC1 DKU3,4 PSU0,1	DPM2 LC1 LC3 LC5 LC7 RDCC0 (HUBC) DKU1,2 DKU5,6	
	0010 0011 0012 0013 0014 0015 0016 0017	DPM3 LC2 LC4 LC6 LC8 RDCC1 DKU3,4 PSU0,1	DPM3 LC1 LC3 LC5 LC7 RDCC0 (HUBC) DKU1,2 DKU5,6	

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FIG. 3B

Add	b7~b4	b3~b0	Remarks
0018	DPM4 LC2	DPM4 LC1	
0019			
000a	LC4	LC3	
000b			
000c	LC6	LC5	
000d			
000e	LC8	LC7	
000f			
	RDCC1	RDCC0	
		(HUBC)	
	DKU3,4	DKU1,2	
	PSU0,1	DKU5,6	
0020	CPM NIC2	CPM NIC1	DKU,PSU : PSB_TB conversion from PSB-TB to PSB
0021			
0022	NIC4	NIC3	
0023			
0024	EIOC1	EIOC0	
0025			
0026			
0027			
	ASCC1	ASCC0	
	NSPC3	NSPC2	
	RDCC1	RDCC0	
	PSU0,1	PSU1,2	

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FIG. 4

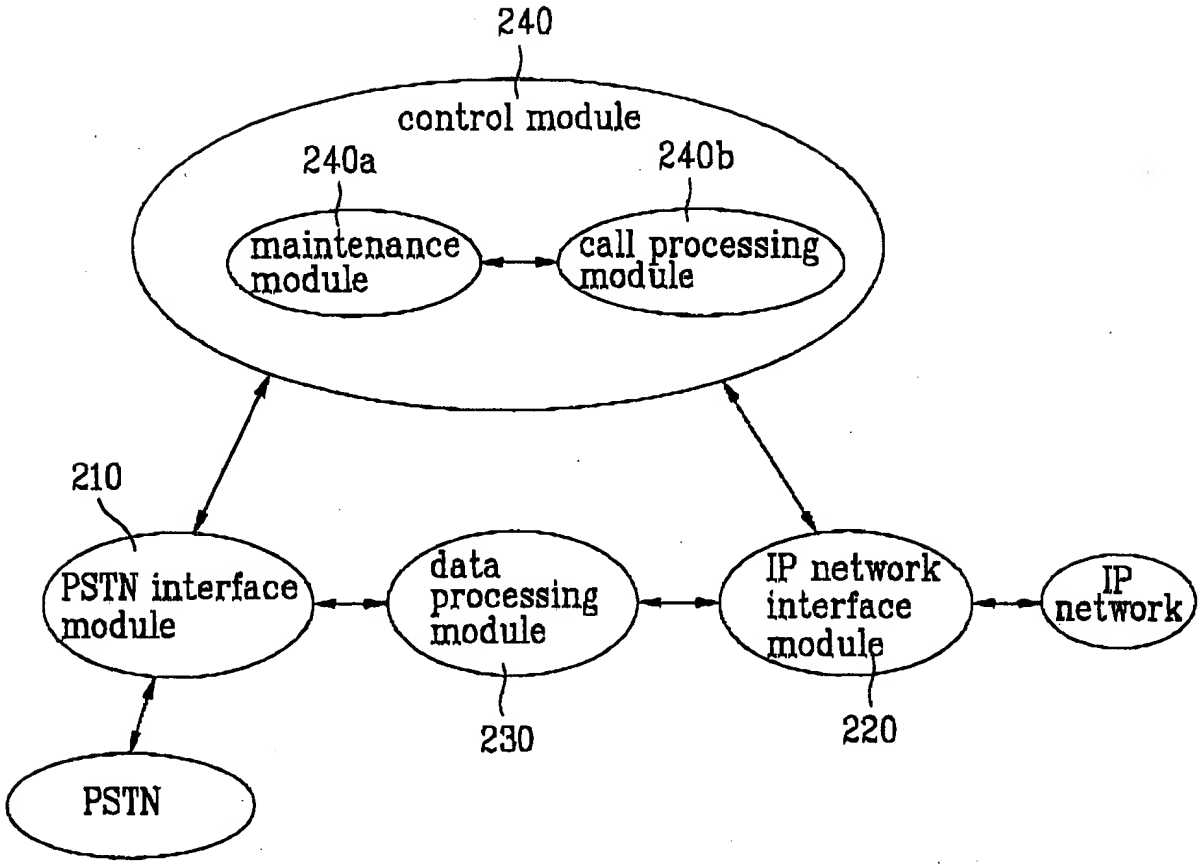

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FIG. 5

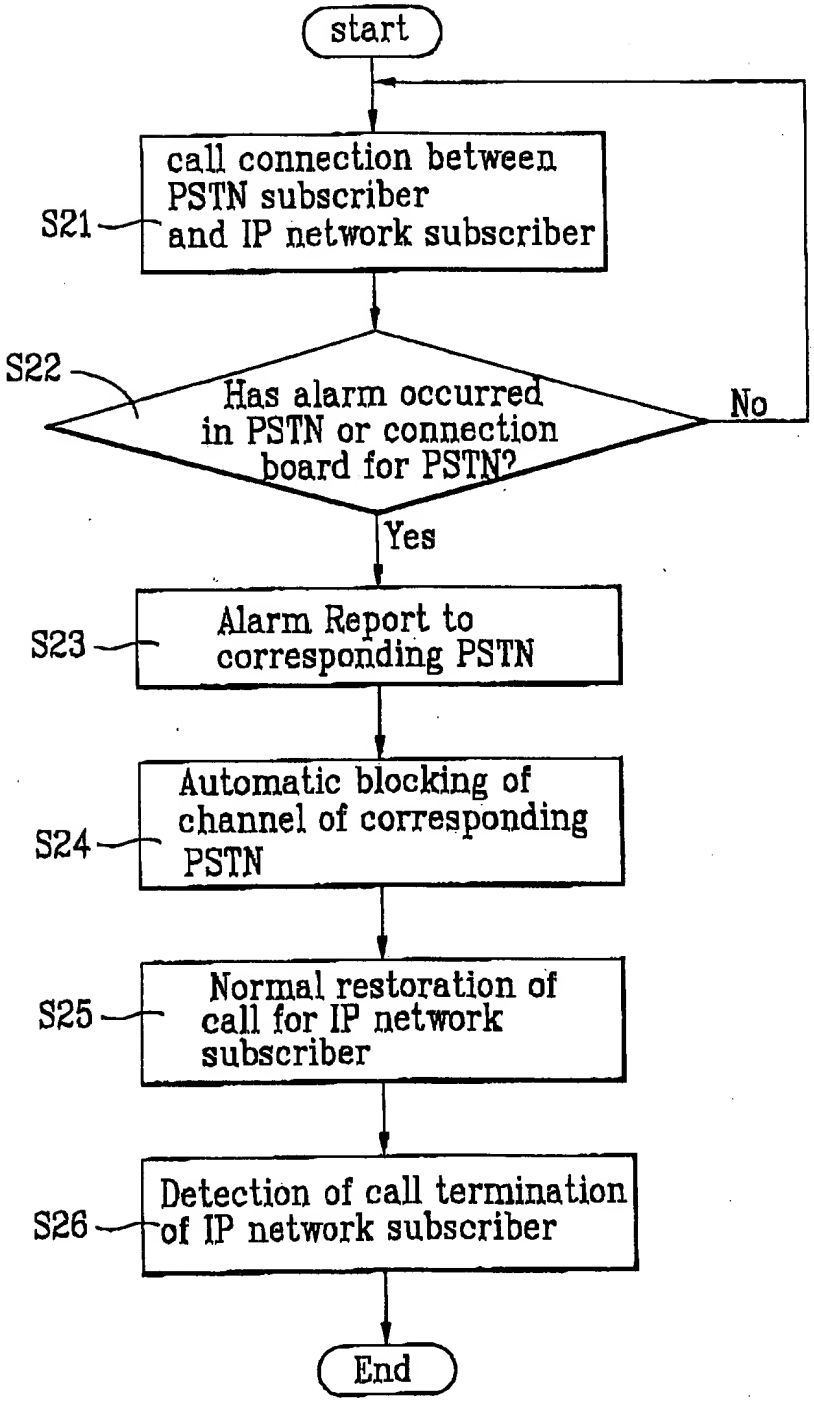

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FIG. 6

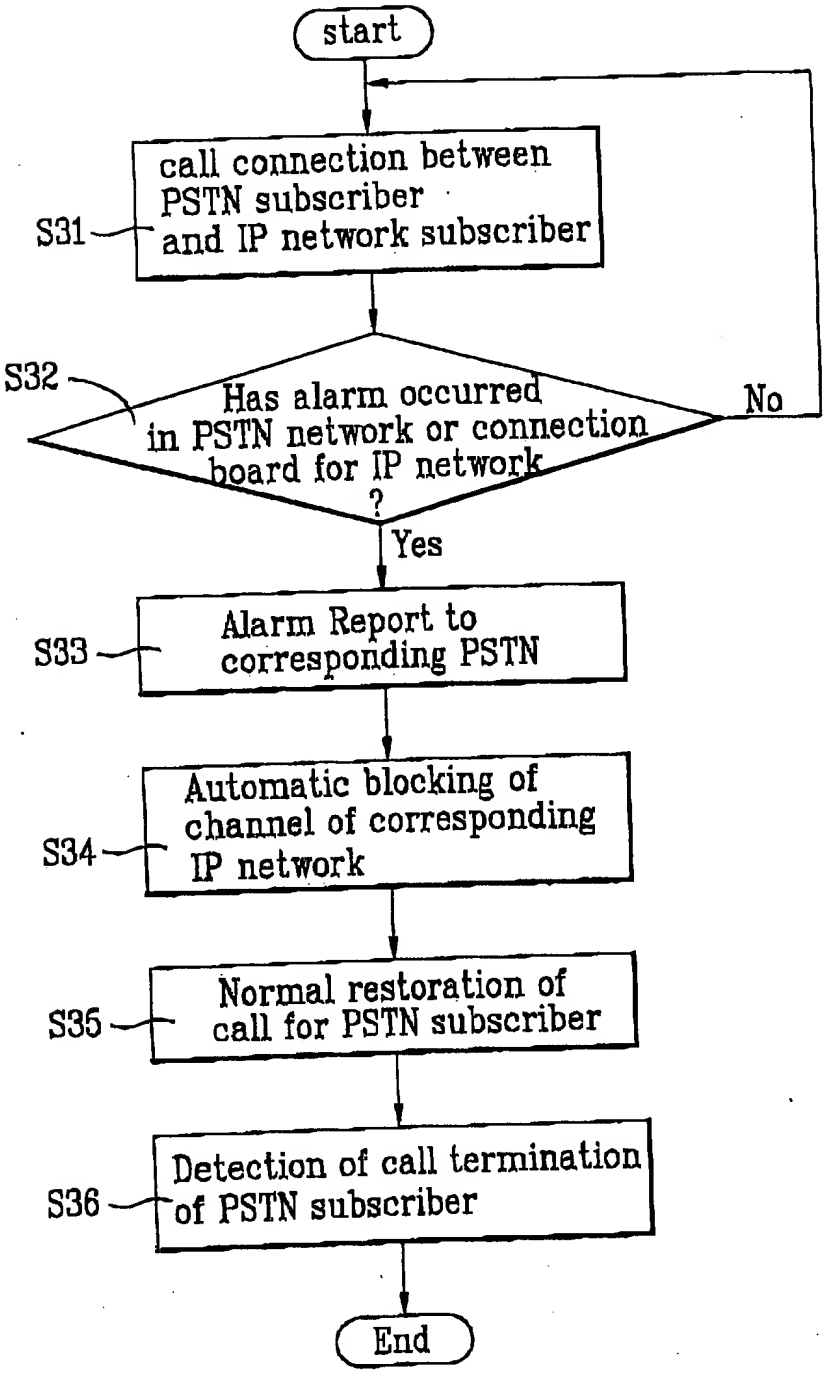

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FIG. 7

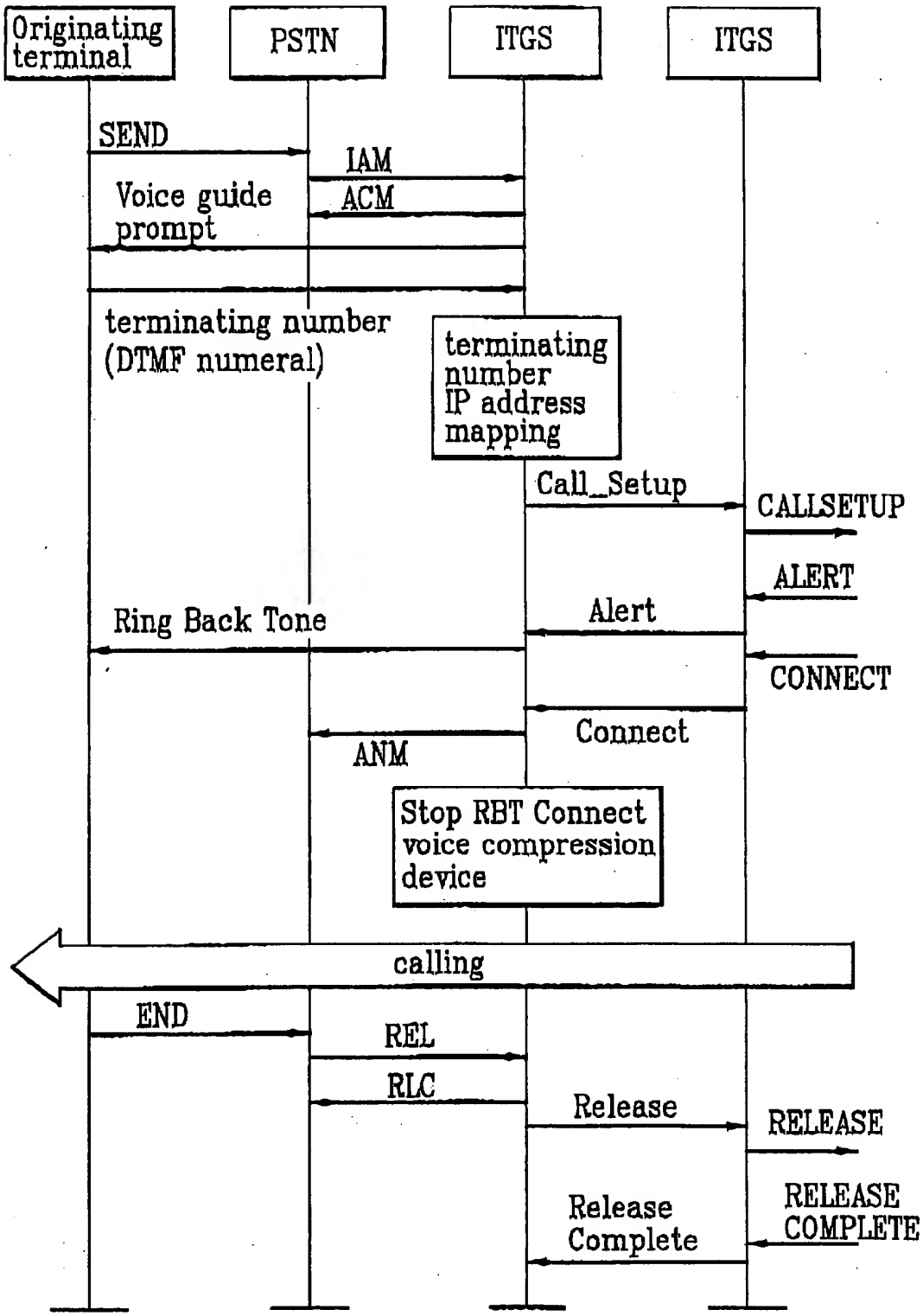

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FIG. 8

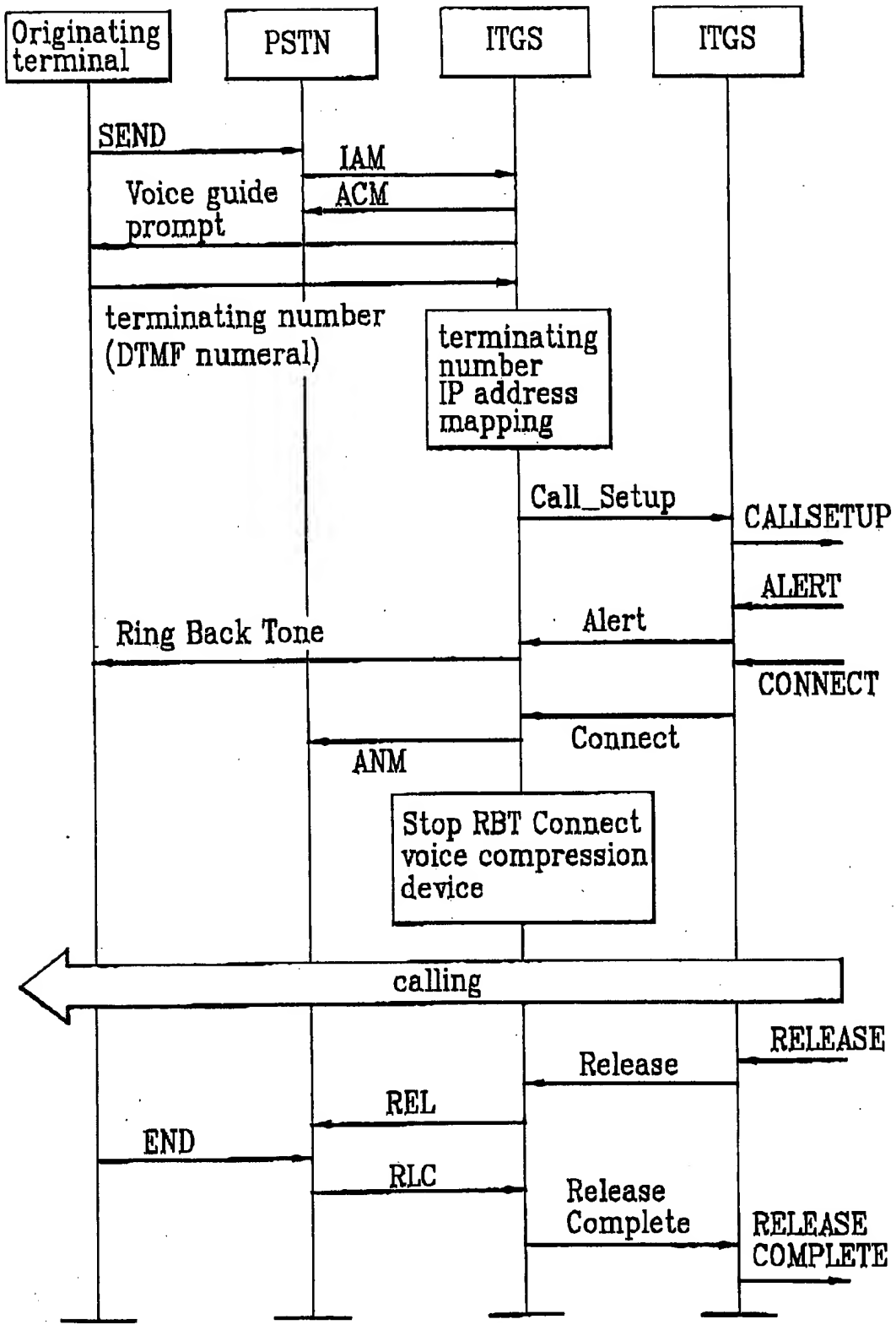

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FIG. 9

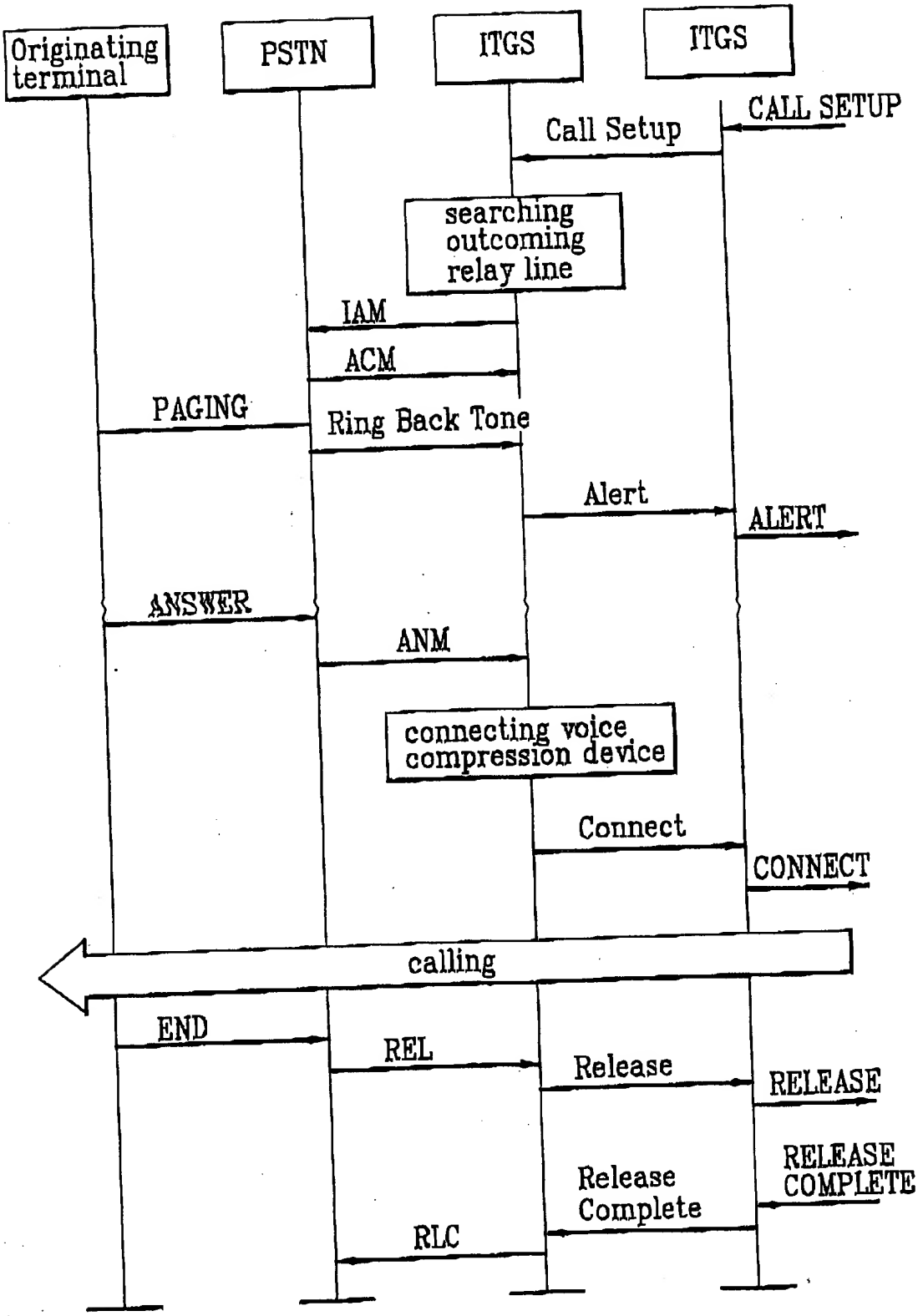

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FIG. 10

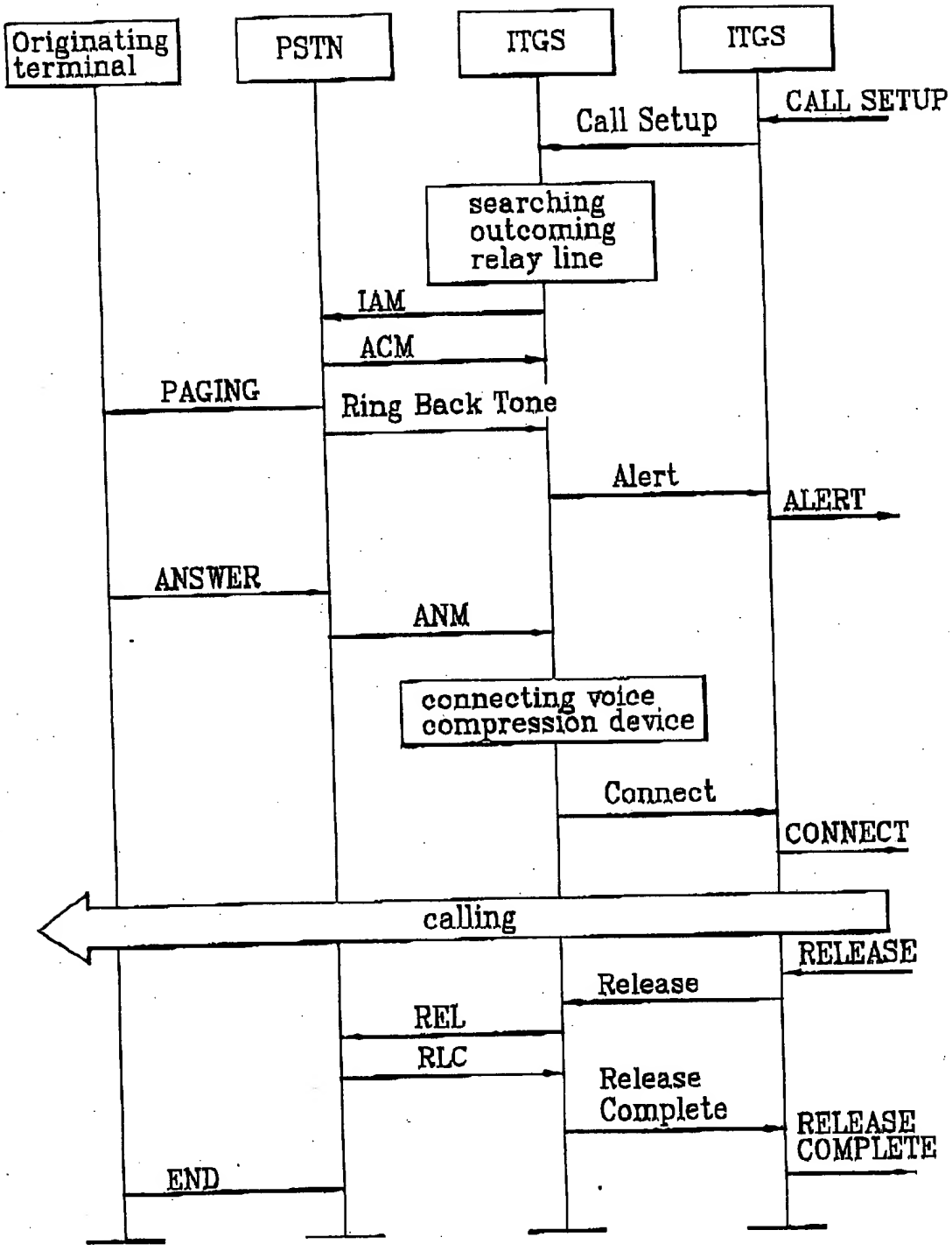

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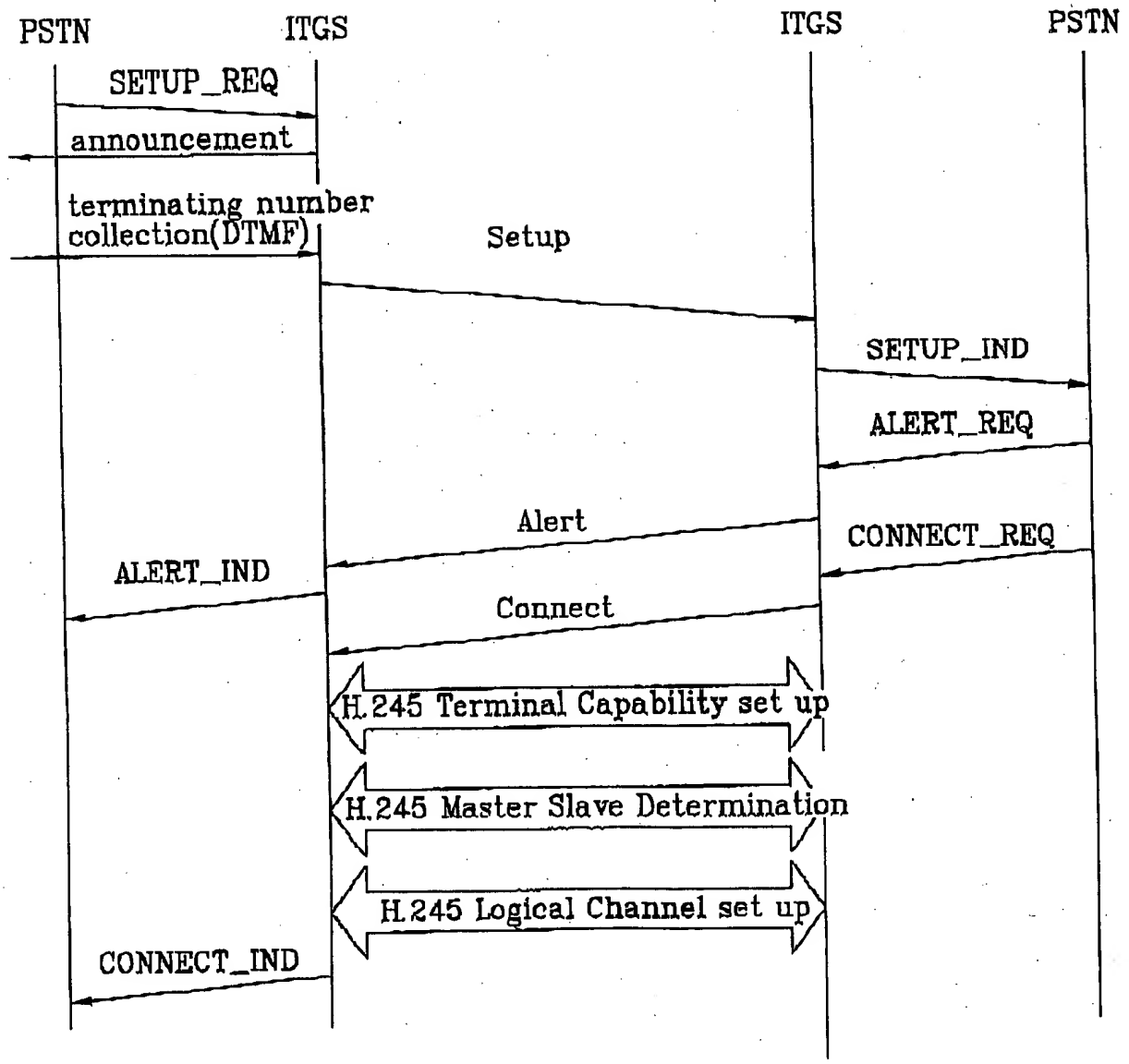
FIG. 11



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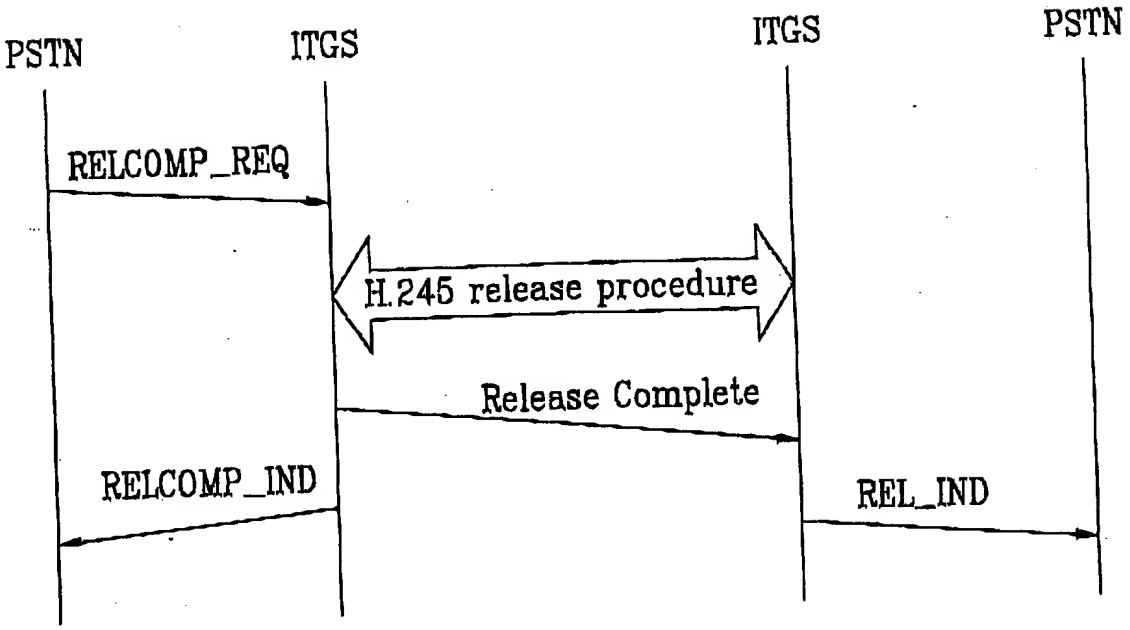
FIG. 12